2122

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Patent Application of	)
	)
Mukesh Patel	) Group Art Unit: Unknown
	)
Application No.: 09/687,777	) Examiner: Unknown
	)
Filed: October 13, 2000	) Confirmation No.: Unknown
	)
For: JAVA HARDWARE ACCELERATOR	)
USING MICROCODE ENGINE	)

THIRD SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT TRANSMITTAL LETTER

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

      Enclosed is a Third Supplemental Information Disclosure Statement and accompanying form PTO-1449 for the above-identified patent application.

[X]   No additional fee for submission of an IDS is required.

[ ]   The fee of $180.00 (126) as set forth in 37 C.F.R. § 1.17(p) is also enclosed.

[ ]   A certification under 37 C.F.R. § 1.97(e) is also enclosed.

[ ]   A certification under 37 C.F.R. § 1.97(e), and the fee of $180.00 (126) as set forth in 37 C.F.R. § 1.17(p) are also enclosed.

[ ]   Charge $_____ to Deposit Account No. 02-4800 for the fee due.

[ ]   A check in the amount of $_____ is enclosed for the fee due.

      The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

By:_____
     Joseph P. O'Malley
     Registration No. 36,226

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Date: July 25, 2002

(05/02)

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Technology Center 2100 JUL 2 9 2002 RECEIVED

THIRD SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicant hereby submits the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

Articles

1. C. JOHN GLOSSNER and STAMATIS VASSILIADIS, *The Delft-Java Engine: An Introduction*, Euro-Part '97, Parallel Processing, Third International Euro-Par Conference, pp. 766-770 (August 1997)

2. M.W. EL-KHARASHI et al., *Java Microprocessor: Computer Architecture Implications*, IEEE Pacific Rim Conference on Communications, Computers and Signal Processing, Vol. 1, pp. 277-280 (August 20-22, 1997)

3. R. TOMASULO, *An Efficient Algorithm for Exploring Multiple Arithmetic Units,* IBM Journal of Research and Development, Vol. 11, No. 1, pp. 25-33 (January 1967)

The documents are being submitted within 3 months of the filing or entry of the national stage of this application or before the first Office Action on the merits, whichever is later, therefore no fee or statement is required under 37 C.F.R. § 1.97(b).

(05/02)

To assist the Examiner, the listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

By: _____ .
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